

Amendments to the Claims:

The listing of claims will replace all prior versions, and listings, of claims in the application:

Listing of Claims:

1. (Currently Amended) A liquid crystal display panel comprising:
light-transmitting first and second electrode substrates; and
a liquid crystal layer held between said first and second electrode substrates and containing liquid crystal molecules whose alignment is controlled from said first and second electrode substrates, wherein said first electrode substrate includes:

a pixel electrode which is disposed at a pixel area and applies an electric field to said liquid crystal layer,

a thin film transistor formed as a switching element for said pixel electrode,
a light-shielding wiring pattern having a plurality of memory wiring layers connected to said pixel electrode and said thin film transistor, and one or more apertures which allow transmission of light applied from a rear side of said first electrode substrate toward the liquid crystal layer, and

a reflection member which reflects incident light applied from a second electrode substrate side through said liquid crystal layer, and overlaps said light-shielding wiring pattern such that said one or more apertures are left unmasked in the pixel area.

2. (Currently Amended) A liquid crystal display panel according to claim 1, wherein said pixel electrode is a transparent electrode located closer to said liquid crystal layer than said light-shielding wiring pattern.

3. (Original) A liquid crystal display panel according to claim 2, wherein said reflection member includes at least one of said memory wiring layer, a reflective material layer formed on said memory wiring layer, and a reflective material layer formed on said transparent electrode.

4. (Currently Amended) A liquid crystal display panel according to claim 2 ~~3~~, wherein said reflective material layer formed on said transparent electrode has a total area accounting for not less than 60% of that of the memory wiring layers.

5. (Currently Amended) A liquid crystal display panel according to claim 2 ~~3~~, wherein said light-shielding wiring pattern further includes a plurality of control wiring layers for said thin film transistor, and said memory wiring layer is formed of the same metallic material as at least one of said control wiring layers.

6. (Original) A liquid crystal display panel according to claim 5, wherein said reflective material layer comprises one of a highly reflective metallic material, a high-melting point metallic material, and a combination of the highly reflective and high-melting point metallic materials.

7. (Original) A liquid crystal display panel according to claim 6, wherein said highly reflective metallic material includes one of aluminum and silver as a major component.

8. (Original) A liquid crystal display panel according to claim 6, wherein said high-

melting point metallic material includes one of molybdenum, tungsten, and a molybdenum-tungsten alloy.

9. (Original) A liquid crystal display panel according to claim 1, wherein said reflection member has a reflection characteristic in which more than 40% of incident light is reflected.

10. (Original) A liquid crystal display panel according to claim 1, wherein said reflection member has a reflection characteristic in which more than 80% of incident light is reflected.

11. (Original) A liquid crystal display panel according to claim 1, wherein said second electrode substrate includes a light diffusion layer which diffuses light.

12. (Original) A liquid crystal display panel according to claim 1, wherein said reflection member includes undulations which diffuse light.

13. (Currently Amended) A liquid crystal display panel according to claim 1, wherein at least one of said first and second ~~electrodes~~ electrode substrates further includes a color filter.

14. (Currently Amended) A liquid crystal display panel according to claim 2, wherein said first electrode substrate further includes ~~another~~ a color filter which covers the light-shielding wiring pattern and serves as an underlayer of said transparent electrode.